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## AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

## LISTING OF CLAIMS:

 (currently amended): A boron phosphide-based semiconductor light-emitting device comprising:

a substrate of silicon single crystal;

a first cubic boron phosphide-based semiconductor layer that is provided on a surface of the substrate and contains twins;

a light-emitting layer that is composed of a hexagonal Group III nitride semiconductor and provided on the first cubic boron phosphide-based semiconductor layer; and

a second cubic boron phosphide-based semiconductor layer that is provided on the lightemitting layer, contains twins and has a conduction type different from that of the first cubic boron phosphide-based semiconductor layer,

wherein the light-emitting layer has a profile of phosphorus atom concentration that gradually decreases from a bottom thereof in a thickness direction, and

a phosphorus atom concentration at a bottom of the light-emitting layer is  $5 \times 10^{18}$  cm<sup>-3</sup> to  $2 \times 10^{20}$  cm<sup>-3</sup>.

 (original): A boron phosphide-based semiconductor light-emitting device according to claim 1, wherein the substrate is a (111)-silicon single-crystal substrate having a

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(111) crystal plane, and the first cubic boron phosphide-based semiconductor layer is provided on the (111) crystal plane.

- (original): A boron phosphide-based semiconductor light-emitting device according to claim 2, wherein the first cubic boron phosphide-based semiconductor layer has a [110] direction aligned with a [110] direction of the silicon single crystal.
- 4. (previously presented): A boron phosphide-based semiconductor light-emitting device according to claim 2, wherein the first cubic boron phosphide-based semiconductor layer contains (111) twins having a (111) crystal plane serving as a twinning plane in a junction area in contact with the (111) crystal plane of the (111)-silicon single-crystal substrate.
- (previously presented): A boron phosphide-based semiconductor light-emitting device according to claim 1, wherein, the first cubic boron phosphide-based semiconductor layer is an undoped layer to which no impurity element has been intentionally added.
- 6. (previously presented): A boron phosphide-based semiconductor light-emitting device according to claim 1, wherein the light-emitting layer has a [-2110] direction aligned with a [110] direction of the first cubic boron phosphide-based semiconductor layer and has a (0001) crystal plane serving as a front surface.
  - 7. (canceled).

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(original): A boron phosphide-based semiconductor light-emitting device 8. according to claim 6, wherein the second cubic boron phosphide-based semiconductor layer has a [110] direction aligned with the [-2110] direction of the light-emitting layer.

- (previously presented): A boron phosphide-based semiconductor light-emitting 9. device according to claim 6, wherein the second cubic boron phosphide-based semiconductor layer contains (111) twins having a (111) crystal plane serving as a twinning plane in a junction area in contact with the (0001) crystal plane of the light-emitting layer.
- (previously presented): A boron phosphide-based semiconductor light-emitting 10. device according to claim 6, wherein the second cubic boron phosphide-based semiconductor layer is an undoped layer to which no impurity element has been intentionally added.
- (previously presented): A boron phosphide-based semiconductor light-emitting 11. device according to claim 1, wherein the first and second cubic boron phosphide-based semiconductor layers exhibit a bandgap at room temperature of 2.8 eV or more.
- (withdrawn): A boron phosphide-based semiconductor light-emitting device 12. according to claim 1, wherein the first and second cubic boron phosphide-based semiconductor layers are provided so as to serve as cladding layers.
- (previously presented): A boron phosphide-based semiconductor light-emitting 13. device according to claim 1, wherein the second cubic boron phosphide-based semiconductor

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diffuse.

layer is provided so as to serve as a window layer which allows passage of light emitted from the light-emitting layer to the outside.

14. (withdrawn): A boron phosphide-based semiconductor light-emitting device according to claim 1, wherein the second cubic boron phosphide-based semiconductor layer is provided so as to serve as a current-diffusion layer which allows device operation current to

15. (withdrawn): A boron phosphide-based semiconductor light-emitting device according to claim 1, wherein the second cubic boron phosphide-based semiconductor layer is provided so as to serve as a contact layer for forming an electrode.